

FIG. 3

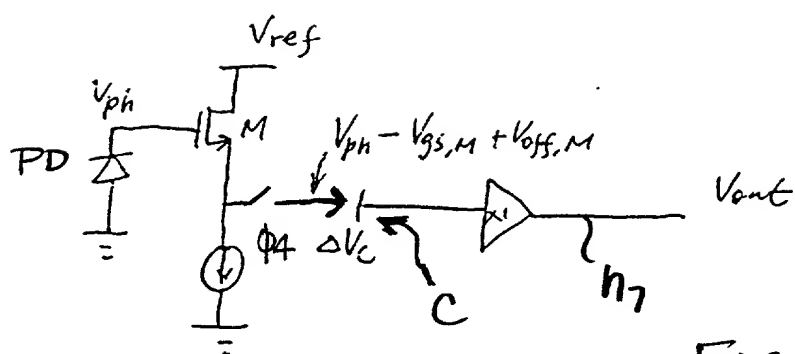


FIG. 4

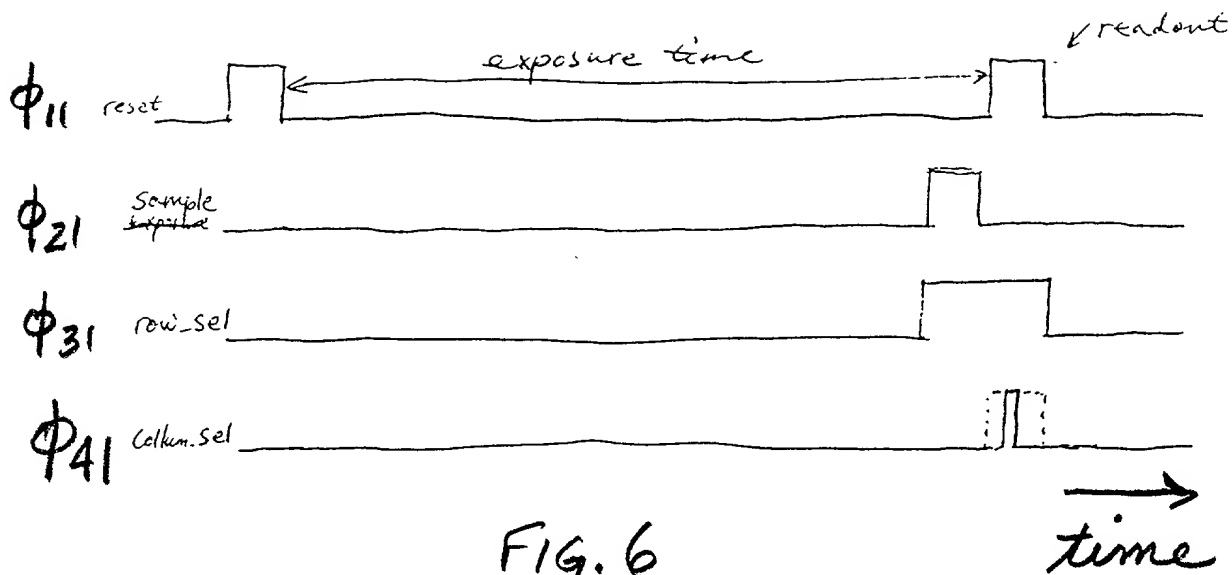


FIG. 6

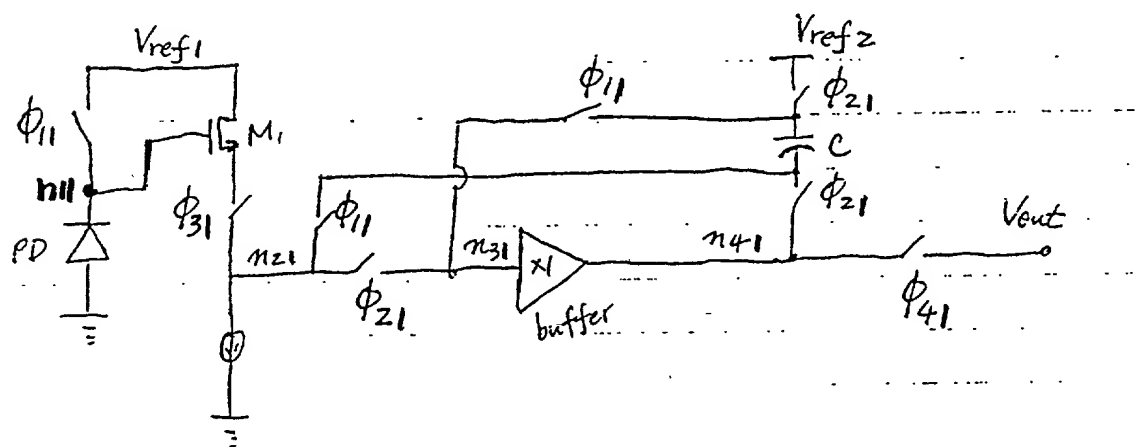


FIG. 5

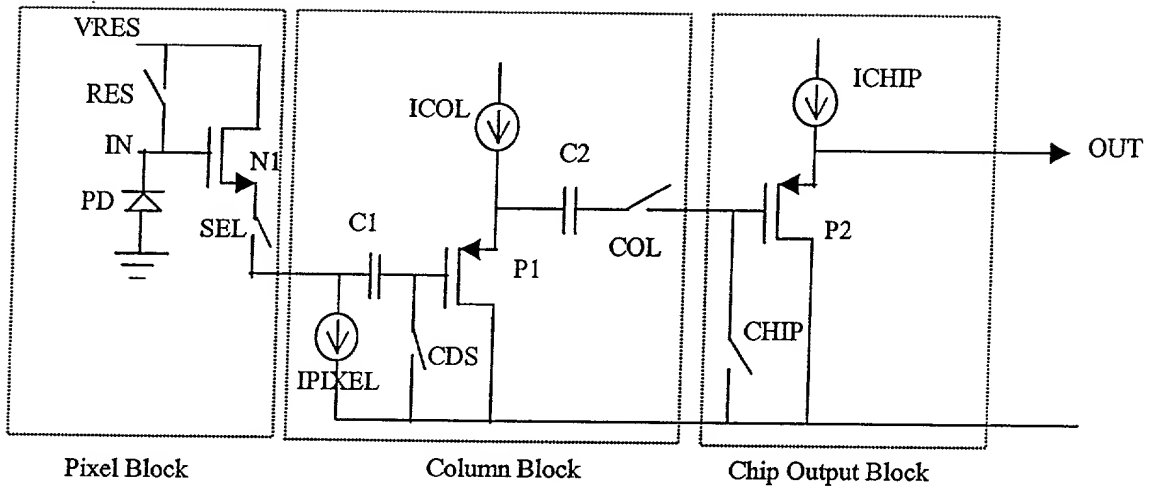


FIG. 9

	RES	SEL	CDS	COL	CHIP
Integration Period	OPEN	OPEN	X	X	X
Readout Step 1	OPEN	OPEN	CLOSED	CLOSED	CLOSED
Readout Step 2	OPEN	CLOSED	CLOSED	OPEN	CLOSED
Readout Step 3	OPEN	CLOSED	OPEN	OPEN	OPEN
Readout Step 4	CLOSED	CLOSED	OPEN	CLOSED	OPEN
Readout Step 5	OPEN	OPEN	OPEN	OPEN	CLOSED
Ready for next Pixel readout					

FIG. 10